TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4066BP,TC4066BF,TC4066BFN,TC4066BFT

#### TC4066B Quad Bilateral Switch

TC4066B contains four independent circuits of bidirectional switches. When control input CONT is set to "H" level, the impedance between input and output of the switch becomes low and when it is set to "L" level, the impedance becomes high. This can be applied for switching of analog signals and digital signals.

- ON-resistance, Ron
  - $250 \ \Omega \ (typ.): VDD VSS = 5 \ V$  $110 \ \Omega \ (typ.): VDD VSS = 10 \ V$  $70 \ \Omega \ (typ.): VDD VSS = 15 \ V$
- OFF-resistance, Roff Roff (typ.) >  $10^9 \Omega$

# **Pin Assignment**



#### **Truth Table**

Control	Impedance between IN/OUT-OUT/IN	(Note)
Н	0.5 to 5 $\times$ 10 $^2$ $\Omega$	
L	>10 <sup>9</sup> Ω	

Note: See static electrical characteris	tics
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Weight	
DIP14-P-300-2.54	: 0.96 g (typ.)
SOP14-P-300-1.27A	: 0.18 g (typ.)
SOP14-P-300-1.27	: 0.18 g (typ.)
SOL14-P-150-1.27	: 0.12 g (typ.)
TSSOP14-P-0044-0.65A	: 0.06 g (typ.)



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### Logic Diagram

1/4 TC4066B



# Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V <sub>DD</sub>	$V_{SS}-0.5$ to $V_{SS}+20$	V
Control input voltage	V <sub>CIN</sub>	$V_{\mbox{\scriptsize SS}}-0.5$ to $V_{\mbox{\scriptsize DD}}+0.5$	V
Switch I/O voltage	V <sub>I/O</sub>	$V_{\mbox{\scriptsize SS}}-0.5$ to $V_{\mbox{\scriptsize DD}}+0.5$	V
Potential difference across I/O during ON	I <sub>I/O</sub>	±0.5	V
Control input current	I <sub>CIN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C
Storage temperature range	T <sub>stg</sub>	–65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

# Operating Range (V<sub>SS</sub> = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V <sub>DD</sub>		3	_	18	V
Input voltage	V <sub>DD</sub> /V <sub>OUT</sub>	_	0		V <sub>DD</sub>	V

Note: The Operating Range are required to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

# Static Electrical Characteristics (in case not specifically appointed, V<sub>SS</sub> = 0 V)

Characteristics			Test Condition		-40°C		25°C			85°C		
		Symbol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
				5	3.5	—	3.5	2.75	_	3.5	—	
Control i high volt	input tage	VIH	I <sub>IS</sub>   < 10 μΑ	10	7.0	—	7.0	5.50	—	7.0	—	V
U	0			15	11.0	—	11.0	8.25	—	11.0	—	
				5	_	1.5	_	2.25	1.5	_	1.5	
Control i low volta	input age	VIL	I <sub>IS</sub>   < 10 μΑ	10	—	3.0	—	4.50	3.0	—	3.0	V
	5-			15	—	4.0	—	6.75	4.0	—	4.0	
			$0 \le V_{IS} \le VDD$ $R_L = 10 \ k\Omega$	5	_	800		290	950	_	1200	
On-state resistant	e ce	R <sub>ON</sub>		10	—	210	—	120	250	—	300	Ω
				15	—	140	—	85	160	—	200	
∆On-state resistance		R <sub>ON</sub> Δ	_	5	_	_	_	10	_	_	_	
				10	_	_	_	6	_	_	_	Ω
(between 2 switch	n any es)			15	_	—	—	4		_	—	
Input/out	tput		$V_{\rm IN} = 18 \text{ V},$	18		±100	_	±0.1	±100		±1000	
leakage current		IOFF	$V_{IN} = 0 V,$ $V_{OUT} = 18 V$	18	—	±100	—	±0.1	±100	—	±1000	nA
				5	_	0.25	_	0.001	0.25	_	7.5	
Quiescent supply current		$V_{\rm IN} = V_{\rm SS}, V_{\rm I}$	$v_{\rm IN} = v_{\rm SS}, v_{\rm DD}$	10	—	0.50	—	0.001	0.50	—	15.0	μA
			(Note)	15	—	1.00	—	0.002	1.00	—	30.0	
Input current	"H" level	lін	V <sub>IH</sub> = 18 V	18	_	0.1	_	10 <sup>-5</sup>	0.1	_	1.0	
	"L" level	IIL	$V_{IL} = 0 V$	18		-0.1	_	-10 <sup>-5</sup>	-0.1		-1.0	μΑ

Note: All valid input combinations.

# Dynamic Electrical Characteristics (Ta = $25^{\circ}$ C, V<sub>SS</sub> = 0 V, C<sub>L</sub> = 50 pF)

		Test Condition							
Characteristics	Symbol				V <sub>DD</sub> (V)	Min	Тур.	Max	Unit
				0	5	_	15	40	
Phase difference between input to output	<b>ф</b> І-О	$C_L = 50 \text{ pF}$		0	10	_	8	20	ns
					15	_	5	15	
Dranagation dolou time	t			0	5	_	55	120	
	чрZL	$K\Gamma = 1 K75$		0	10	—	25	40	ns
	чрΖН	CL = 20 hL		0	15	—	20	30	
Dropostion dolou time	<b>f</b>			0	5	_	45	80	
	لېLZ • ب−	$K\Gamma = 1 K75$		0	10	_	30	70	ns
	<sup>t</sup> pHZ	CL = 20 hL		0	15	—	25	60	
	f <sub>max</sub> (C)			0	5	_	10		
Max control input repetition rate		$K\Gamma = 1 K75$		0	10	_	12	—	MHz
		CL = 20 hL		0	15	—	12	—	
	f <sub>max</sub> (I-O)	$R_L = 1 \ k\Omega$		5	Б		30		MHz
		C <sub>L</sub> = 15 pF	(Note 1)	-0	5				
Total harmonic distortion		$R_L = 10 \ k\Omega$		5 5			0.03		0/
		f = 1 kHz	(Note 2)	-5	5		0.03		70
-50dB feed through frequency		$R_L = 1 \ k\Omega$	(Note 3)	-5	5	_	600	_	kHz
-50dB crosstalk frequency		$R_L = 1 \ k\Omega$	(Note 4)	-5	5	_	1	_	MHz
Crosstalk		$R_{IN} = 1 \ k\Omega$		0	5	_	200		
	—	$R_{OUT} = 10 \ k\Omega$	0	10	—	400	—	mV	
		C <sub>L</sub> = 15 pF	0	15	_	600	_	ĺ	
		Control input				_	5	7.5	~F
input capacitance	CIN	Switch I/O				_	10		Pi
Feed through capacitance	C <sub>IN-OUT</sub>						0.5	_	pF

Note 1: Sine wave of ±2.5 <sub>p-p</sub> shall be used for V<sub>is</sub> and the frequency of 20 log 10  $\frac{V_{OS}}{V_{is}}$  = -3dB shall be f<sub>max</sub>.

Note 2: V<sub>is</sub> shall be sine wave of  $\pm 2.5$  V<sub>p-p</sub>

- Note 3: Sine wave of  $\pm 2.5 V_{p-p}$  shall be used for  $V_{is}$  and the frequency of 20 log 10  $\frac{V_{OUT}}{V_{is}} = -50$ dB shall be feed-through.
- Note 4: Sine wave of  $\pm 2.5 V_{p-p}$  shall be used for  $V_{is}$  and the frequency of 20 log 10  $\frac{V_{OUT}}{V_{is}} = -50 dB$  shall be crosstalk.

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#### **Circuit for Measurement of Electrical Characteristics**

1. φ<sub>I-O</sub>





### $2. \quad t_{pZH}, \, t_{pHL}, \, t_{pLZ}, \, t_{pHZ}$





#### 3. RON





4. fmax (C)



### 5. Crosstalk between Any Two Switches

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6. Crosstalk, Control to Input



#### 7. Total Harmonic Distortion, fmax (I-O), Feedthrough



DIP14-P-300-2.54

Unit : mm



Weight: 0.96 g (typ.)



SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)



Weight: 0.18 g (typ.)

### Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

TSSOP14-P-0044-0.65A

Unit: mm



Weight: 0.06 g (typ.)

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